CURRICULUM VITAE

University of Idaho

NAME: Timothy Murphy DATE: January 2023

RANK OR TITLE: Assistant Professor of Law

DEPARTMENT: College of Law

OFFICE LOCATION AND CAMPUS ZIP: OFFICE PHONE: 208-364-4543

Rm. 343 FAX:

501 W. Front St. EMAIL: temurphy@uidaho.edu

Boise, ID 83702 WEB

DATE OF FIRST EMPLOYMENT AT UI: July 1, 2018

DATE OF TENURE: (Year or untenured) Untenured

DATE OF PRESENT RANK OR TITLE: August 15, 2021

EDUCATION BEYOND HIGH SCHOOL:

Degrees: (List most recent degree first: Degree, institution name, city, state, date, major or area of specialization.)

- J.D., University of Michigan Law School, Ann Arbor, Michigan, May 1, 2008, Law
- M.S.E.E., University of Michigan, Ann Arbor, Michigan, April 30, 2004, Electrical engineering/optoelectronics
- B.S.E.E., Boise State University, Boise, Idaho, May 12, 2002, Electrical engineering

Certificates and Licenses:

- Attorney, Idaho State Bar
- Patent Attorney, U.S. Patent and Trademark Office

EXPERIENCE:

Teaching, Extension and Research Appointments: (List position titles and locations since receipt of Bachelor's degree)

- Assistant Professor of Law, University of Idaho College of Law, 2021 to Present
- Assistant Clinical Professor of Law, University of Michigan Law School, July 2020 to May 2021
- Temporary Faculty and Director of the Entrepreneurship Law Clinic, University of Idaho College of Law, August 2018 to May 2020

Academic Administrative Appointments: (List position titles and locations since receipt of Bachelor's degree)

• None

Non-Academic Employment including Armed Forces: (List title, brief description, date)

- Sr. Patent and Trademark Counsel, Micron Technology, Inc., Boise, Idaho, August 2010-August 2018
- Associate Attorney, Zarian Midgley & Johnson, Boise, Idaho, January 2010-August 2010
- Associate Attorney, Marger Johnson & McCollum, Portland, Oregon, May 2007-December 2009
- Manufacturing Engineer, Micron Technology, Inc., Boise, Idaho, July 1995-May 2002
- Powerplant Operator, Idaho Power, Hagerman, Idaho, August 1994-July 1995
- Machinist's Mate, U.S. Navy, Norfolk, Virginia, June 1988-June 1994

Consulting: (List company/institute name, title, brief description, date)

• None

TEACHING ACCOMPLISHMENTS: (Academic and Extension teaching)

Areas of Specialization: Contracts and Intellectual Property

Courses Taught: (title, course number, date(s))

- Contracts I, LAW 813, Fall '21 and '22
- Contracts II, LAW 814, Spring '22 and '23
- Introduction to Intellectual Property, LAW 903, Fall '21 and '22
- Patents, LAW 931, Spring '19
- Patent Drafting, LAW 991, Spring '19
- Lawyering Process: Transactions, LAW 991, Fall '19
- Entrepreneurship Law Clinic, LAW 978, Fall '18 and '19 and Spring '19 and '20
- Internet Law, LAW 918, Spring '20

Students Advised:

Undergraduate Students: (advised to completion of degree, number per year)

N/A

Graduate Students:

Advised to completion of degree-major professor (student name, degree, and date) N/A Served on graduate committee (student name, degree, and date) N/A

Materials Developed: (non-scholarship activity)

Courses Developed:

- Patent Drafting, LAW 991
- Lawyering Process: Transactions, LAW 991

Non-credit Classes, Workshops, Seminars, Invited Lectures, etc.:

• Jurisprudence Reading Group (Spring 2023)

Honors and Awards:

SCHOLARSHIP ACCOMPLISHMENTS: (Including scholarship of teaching and learning, artistic creativity, discovery, and application/integration)

Publications, Exhibitions, Performances, Recitals:

Refereed/Adjudicated: (i.e. books, book chaps., journals, proc., abstr., etc.; provide citations-author, date, title, publisher)

- Timothy Murphy, Memorizing Trade Secrets, 57 Rich. L. Rev. (2023).
- Timothy Murphy, How Can a Departing Employee Misappropriate Their Own Creative Outputs?, 66 Vill. L. Rev. 529 (2021).
- Timothy Murphy, An Instance of Open Hardware: A Different Approach to Free and Open Source Hardware Licensing, 30 Fordham Intell. Prop. Media & Ent. L. J. 1045 (2020).
- Timothy E. Murphy, University of Idaho's Entrepreneurship Law Clinic: Providing Free Legal Services for Idaho Ventures, The Advocate, Vol. 62, No. 3/4, at p. 34.

Peer Reviewed/Evaluated: (i.e. journals, articles, proceedings, abstracts, etc.)

- T.E. Murphy, D.Y. Chen, E. Cagin, and J.D. Phillips, Electronic Properties Of ZnO Epilayers Grown On C-Plane Sapphire By Plasma-Assisted Molecular Beam Epitaxy, Proceedings from the 22nd North American Molecular Beam Epitaxy Conference, October 10-13, 2004, Banff, Canada, Journal of Vacuum Science and Technology B, Volume 23 (3), 1277-1280 (2005).
- T.E. Murphy, D.Y. Chen, and J.D. Phillips, Growth And Electronic Properties Of ZnO Epilayers By Plasma-Assisted Molecular Beam Epitaxy, Proceedings from the 2004 U.S. Workshop on the Physics and Chemistry of

II-VI Materials, October 5-7, 2004, Chicago, Illinois, Journal of Electronic Materials, Volume 34 (6), 699-703 (2005).

- T. E. Murphy, J. O. Blaszczak, K. Moazzami, W. E. Bowen, And J. D. Phillips, Properties Of Electrical Contacts On Bulk And Epitaxial n-Type ZnO, Proceedings from the 46th TMS Electronic Materials Conference, June 23-25, 2004, Notre Dame, Indiana, Journal of Electronic Materials, Volume 34 (4), 389-394 (2005).
- T.E. Murphy, S. Walavalkar, and J.D. Phillips, Epitaxial growth and surface modeling of ZnO on c-plane Al2O3, Applied Physics Letters, Volume 85 (26), 6338-6340 (2004).
- T.E. Murphy, D.Y. Chen, and J.D. Phillips, Electronic Properties Of Ferroelectric BaTiO3/MgO Capacitors On GaAs, Applied Physics Letters, Volume 85 (15), 3208-3210 (2004).
- D. Chen, T. E. Murphy, S. Chakrabarti, and J. D. Phillips, Optical Waveguiding In BaTiO3/MgO/AlxOy/GaAs Heterostructures, Applied Physics Letters, Volume 85 (22), 5206-5208 (2004).
- D.Y. Chen, T.E. Murphy, and J.D. Phillips, Deposition Of BaTiO3 Thin Films And MgO Buffer Layers On Patterned GaAs Substrates for Integrated Optics Applications, Materials Research Society 2003 Fall Meeting Proceedings, Volume 784, C11.23 (2004).
- T.E. Murphy, D.Y. Chen, and J.D. Phillips, Integration of BaTiO3 Ferroelectric Thin Films with GaAs for Functional Devices, Proceedings of the 15th Biennial University/Government/Industry Microelectronics Symposium, June 30-July 2, 2003, Boise, Idaho (2003).

Other: (reports, proceedings, papers, citations and references, performances)

Refereed/Adjudicated (currently scheduled or submitted): (provide citations)

Peer Reviewed/Evaluated (currently scheduled or submitted):

Presentations and Other Creative Activities: (i.e. slide sets, web pages, video productions, etc., provide date and location)

Professional Meeting Papers, Workshops, Showings, Recitals: (provide date and location)

- Taxonomy of Remembered Trade Secret Information, 2023 Trade Secret Workshop, January 13, 2023
- Introducing Skills Exercises in First-Year Contracts, Faculty Teaching Retreat, March 25, 2022
- Employee Trade Secret Issues, 2022 Trade Secret Workshop, June 10-11, 2022
- Memorizing Trade Secrets, WIPIP 2022, Feb. 18-19, 2022
- Trade Secrets Liability for Remembered Information, Junior IP Scholars Association Workshop, July 22-23, 2021
- Remote Teaching Approaches for Inclusivity in Transactional Clinic Seminars, Northwest Clinical Law Conference, Nov. 5-7, 2020.

Patents: (provide title/description, patent number and date)

- US Patent Number 7,732,882, Method and system for electrically coupling a chip to chip package, issued June 8, 2010.
- US Patent Number 7,489,875, System and method for multiple bit optical data transmission in memory systems, issued February 10, 2009.
- US Patent Number 7,355,273, Semiconductor dice having back side redistribution layer accessed using through-silicon vias, methods, issued April 8, 2008.
- US Patent Number 7,335,985, Method and system for electrically coupling a chip to chip package, issued February 26, 2008.
- US Patent Number 7,254,331, System and method for multiple bit optical data transmission in memory systems, issued August 7, 2007.
- US Patent Number 7,015,559, Method and system for electrically coupling a chip to chip package, issued March 21, 2006.
- US Patent Number 6,936,489, Method and system for electrically coupling a chip to chip package, issued August 30, 2005.
- US Patent Number 6,914,317, Thin microelectronic substrates and methods of manufacture, issued July 5, 2005.
- US Patent Number 6,831,301, Method and system for electrically coupling a chip to chip package, issued December 14, 2004.

• US Patent Number 6,800,930, Semiconductor dice having back side redistribution layer accessed using through-silicon vias, and assemblies, issued October 5, 2004.

- US Patent Number 6,693,342, Thin microelectronic substrates and methods of manufacture, divisional from 6,303,469, issued February 17, 2004.
- US Patent Number 6,303,469, Thin microelectronic substrates and methods of manufacture, issued October 16, 2001.

Grants and Contracts Awarded: (provide principal and co investigators, title, sponsor, funding dates, amount)

Honors and Awards:

SERVICE:

Major Committee Assignments: (National, State, District, County, University, College, Departmental and dates)

- U of I Faculty Senate, Senator for SW Idaho Region, Fall 2022 to present
 - o Spread Pay Subcommittee, member, Fall 2022 to present
- University Assessment and Accreditation Committee, member, Fall 2021 to present
- University Safety and Loss-Control Committee, member, Fall 2022 to present
- College of Law Academic Hearing Board, chair, Fall 2022-present
- College of Law Curriculum Committee, member, Fall 2022-present
 - o UDWR Subcommittee, member, Fall 2022
 - First-Year Curriculum Subcommittee, Fall 2022-present
- Search Committee for new University Ombuds (Summer 2022)
- College of Law Technology Committee, member, AY 2021-22
- Michigan Patent Pro Bono Program Review Committee (2020-2021)
- Idaho Volunteer Lawyers Program, Policy Council member (2010-2020)
- Idaho Technology Council Tech2Market Committee, member (2018-2020)
- Idaho Military Legal Alliance, board member (2018-2020)
- AALS Clinical Section, Membership, Outreach, and Training Committee, member (2019-2020)

Professional and Scholarly Organizations (including memberships, committee assignments, editorial services, offices held and dates)

- Intellectual Property Section of the Idaho State Bar, Treasurer (2021-22), past Vice-Chair, Secretary, and member (2010-2020)
- Business and Corporate Law Section of the Idaho State Bar, member (2018-2019)
- Child Protection Section of the Idaho State Bar, member (2018-2019)

Outreach Service: (Including popular press, interview articles, newspaper articles, workshops-seminars-tours organized, Extension impact statements)

- AALS Conference on Clinical Legal Education, working group facilitator (May 2019)
- Northwest Clinical Law Conference, Planning Committee member (May-Nov. 2019)

Community Service: (non-academic unrelated to employment)

- Pro Bono Legal Work
 - o Guardian ad litem representations in child protection cases (2014-2019)
 - Hague Convention child abduction case (2016-2017)
 - o Divorce/custody cases (2009-2016)
 - o Small business dispute for refugee family (2014)
 - o § 1983 prisoner civil rights claim (2009-2010)

Honors and Awards:

• Denise O-Donnell-Day Pro Bono Award (2011)

PROFESSIONAL DEVELOPMENT: (workshops and seminars attended)

Teaching:

- AALS Annual Conference, January 2023
- AALS Conference on Clinical Legal Education, May 2019
- Northwest Clinical Law Conference, November 2018, 2019, and 2020

Scholarship:

- Trade Secrets Workshop, January 2023
- Eighth Annual IP Mosaic Conference, October 2022
- Trade Secrets Workshop, June 2022
- WIPIP 2022, February 2022
- Junior IP Scholars Association Workshop, July 2021

Outreach:

Administration/Management: